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DESIGN AND DEVELOPMENT OF RFID READER FRONT-END AND CHIPLESS TAG USING ULTRA WIDE BAND TECHNOLOGY

BY

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ABSTRACT

The invention of the Radio Frequency Identification (RFID) has introduced many advantages in the product tagging, object tracking and supply chain management comparing with the barcode system. Allocation of the UWB frequency range has given the RFID researchers another edge to include more bits for the inscription due to the wideness of the band and nowadays research on RFID is at the forefront to replace the barcode system. Often, it has been seen from literature that in the RFID system, the main focus is on the tag and how it can be read. Many researchers tried to read the backscattered data from the passive UWB tag with highly sophisticated test instrumentations such as, vector network analyzers (VNA), spectrum analyzers etc. In the UWB RFID readers, that can successfully read the microstrip resonator type tag, the generation of the chirp signal (and also in the tag space) limits the inclusion of more bits to the system. Inclusion of more bits needs more bandwidth which leads to the difficulties to design large bandwidth chirp signal. In literature it is also found that there are very little emphasis given on the development on the reader while developing a RFID system and how to make it simple and energy efficient. UWB RFID system is totally dependent on the constant chirp generation from the UWB RFID reader. With respect to the design of the tag, the bits can be spread over the entire bandwidth (BW) which goes up to 5 GHz. To detect the bits, the entire bandwidth needs to be swept while the only concerning frequencies are needed for detection and rest of the BW will be idle. From the theory of the mixer topology it is well known that with the fundamental frequency, two different main tones can be generated. These two tones are totally separate frequencies and can operate individually by separating them with narrow band pass filters. With this idea, by cascading as many mixer-filter configurations eight different frequencies are generated from 5 to 7 GHz. The generation is only limited on those concerning frequencies which the bits are allocated on a microstrip resonator type tag. A duty cycle of 50% is kept while switching different frequencies. Since the generated interrogation signal is a 'chirp and hold' signal, in this process the entire BW sweep is not necessary. Due to 50% on time, the EIRP of the reader is doubled and consequently the reading range is increased. The reader is also made less complex and easy to design. A new type of linear resonator for the UWB passive tag also designed and developed to fit with the designed reader front-end. The result shows the 8-bit tag's resonator section occupies 19.22 mm²/bit. The new design has reduced the area/bit upto 20% from the recent research.

خلاصة البحث

قدم اختراع تقنية تحديد الهوية باستخدام موجات الراديو (RFID) العديد من المزايا في وضع العلامات على المنتج وتتبع الكائن وإدارة سلسلة الإمداد مقارنة بنظام الشفرة الخيطية. أعطي توزيع نطاق الترددات UWB حداً آخر للباحثين في RFID ليشمل المزيد من البت لإدخال البيانات بسبب اتساع النطاق، والبحوث على RFID في الطليعة في الوقت الحاضر لتحل محل نظام الشفرة الخيطية. لقد لوحِظَ من البحوث السابقة أن التركيز الرئيسي في نظام RFID هو على العلامة وكيف يمكن قراءتها. حاول العديد من الباحثين قراءة البيانات المشتتة من علامة UWB بأدوات اختبار متطورة للغاية مثل أجهزة تحليل الشبكات المتجهة (VNA)، تحليل الطيف، إلخ. في قارئ UWB RFID الذي يمكن أن يقرأ بنجاح علامة نوع رنان شريط النقل الكهربائي، يحدّ توليد إشارة chirp (وأيضا في حيّز العلامة) من إدراج المزيد من البت للنظام. يحتاج إدراج المزيد من البت إلى المزيد من عرض النطاق الترددي مما يؤدي إلى صعوبات في تصميم إشارة نطاق ترددي كبير لِ chirp. وُجد أيضاً في البحوث السابقة أن هناك القليل من التركيز على التطوير بالقارئ في الوقت الذي يتم فيه تطوير نظام RFID وكيفية جعله بسيطاً وذات كفاءة في توفير الطاقة. يعتمد نظام UWB RFID كلَّياً على جيل chirp الثابت من قارئ UWB RFID. وفيما يتعلق بتصميم العلامة، يمكن توزيع البتّات على عرض النطاق الترددي بأكمله (BW) والذي يصل إلى 5 غيغاهرتز. للكشف عن البتَّات، يجب أن يتم فحص عرض النطاق الترددي بأكمله في حين أن هناك حاجة فقط للترددات اللازمة لعملية الكشف وسيكون بقية BW خاملاً. من المعروف جيداً من نظرية طوبولوجيا الخلاط أنه يمكن توليد اثنين من الترددات الرئيسية المختلفة مع التردد الأساسي. هاتان الإشارتان هما ترددات منفصلة تماماً ويمكنهما العمل بشكل فردى عن طريق فصلهمًا بمرشحات تمرير نطاق ضيق. بهذه الفكرة، يتم إنشاء ثمانية ترددات مختلفة من 5 إلى 7 غيغاهر تز من خلال تتالى العديد من تكوينات mixer-filter. يقتصر الجيل فقط على الترددات التي تخصتصها البتّات على علامة نوع رنان شريط النقل الكهربائي. يتم الاحتفاظ بدورة العمل بنسبة 50٪ أثناء تبديل الترددات المختلفة. بما أن إشارة الاستجواب الموّلدة هي إشارة "'chirp and hold "، فإن فحص BW الكامل ليس ضرورياً في هذه العملية. نظراً إلى 50٪ في الوقت المحدد، يتم مضاعفة EIRP من القارئ، وبالتالي يتم زيادة نطاق القراءة. جُعِل القارئ أيضاً أقل تعقيداً وسهل التصميم. صُمَّم وطُوَّر نوع جديد من الرنان الخطى للعلامة السلبية UWB ليتناسب مع القارئ الأمامي المُصمّم. تظهر النتيجة أن قسم ريان العلامة 8-بت تشغل 19.22 مم2/بت. خَفَّض التصميم الجديد من المساحة لكل بت لتصل إلى 20٪ من البحوث الأخيرة.

APPROVAL PAGE

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I hereby declare that this dissertation is the result of my own investigations, except where otherwise stated. I also declare that it has not been previously or concurrently submitted as a whole for any other degrees at IIUM or other institutions.

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TABLE OF CONTENTS

Abstracti	
Abstract in Arabicii	
Approval Pageiii	i
Declarationiv	
Acknowledgementsvi	i
Table of contentsvi	i
List of Tablesix	
List of Figuresx	
List of Abbreviationsxi	ii
List of Symbols	V
1.1 Background11.2 Problem Statement51.3 Research Philosophy81.4 Objectives of Study91.5 Methodology91.6 Research Scope101.7 Thesis Organisation10)
CHAPTER TWO : LITERATURE REVIEW 12 2.1 Introduction 12 2.2 Components of RFID System 12 2.3 Different Frequency Bands in RFID Systems 13 2.3.1 Low Frequency Band 14 2.3.2 High Frequency Band 14 2.3.3 Ultra High Frequency Band 14 2.3.4 Microwave Band 14 2.3.5 Ultra Wide Band (UWB) 15 2.4 RFID Tags Based On Power Supply 17 2.4.1 Active Tags 17 2.4.3 Semi-Active or Battery-Assisted Passive (BAP) Tag 12	223444557791
2.5 Review in tags in UWB RFID	5

2.5.3 Embedded Singularity Based	
2.5.4 UWB Impulsed Interrogation Based	35
2.5.5 Multi-resonant High-Impedance Surfaces Based	
2.5.6 Complex-Natural-Resonance (CNR) Based	40
2.6 Review on RFID Readers	43
2.6.1 RFID Reader Components	44
2.6.2 Classification of RFID Readers	46
2.6.3 Existing RFID Readers	
2.7 Chapter Summary	57
CHAPTER THREE : DESIGN AND CONSIDERATION	59
3.1 Introduction	59
3.2 Development of the Reader Front-End	60
3.3 Transmitter	60
3.3.1 Signal Generation Section (SGS)	61
3.3.1 Design of Individual Blocks	63
3.3.2 Switching Section	69
3.3.3 Microwave Power Amplifier (PA) Design	70
3.4 Receiver	78
3.4.1 Low Noise Amplifier (LNA) Design	78
3.4.2 Signal Detector Section	80
3.5 Developement of the Tag	
3.5.1 The Design Consideration	
-	
3.6 chapter Summarry	
3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	86
3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION 4.1 Introduction	
 3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summary CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summary	
 3.6 chapter Summary CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summary CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summary CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summary CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	
 3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	
3.6 chapter Summarry CHAPTER FOUR : RESULTS AND DISCUSSION	

LIST OF TABLES

Table No.	Page No.
Table 2.1: Features of different RFID systems	16
Table 2.2: Comparison of Different RFID system (w.r.t. Battery on board)	21
Table 2.3: Summery of different UWB RFID tag found in literatures	42
Table 2.4: Comparison between different RFID reading processes	56
Table 3.1: Even and Odd mode Impedance for BPF	68
Table 4.1: Comparison between existing works	101

LIST OF FIGURES

Figure No.	Page No.
Figure 1.1: The basic block diagram of an RFID system	4
Figure 2.1: Components of RFID Systems	13
Figure 2.2: The types of RFID Tags	17
Figure 2.3: A simple block diagram of an active RFID tag	18
Figure 2.4: (a) SAW and (b) Multi-resonator based RFID Reader-Tag system	20
Figure 2.5: Physical concept of Chipless RFID	25
Figure 2.6: Multi-resonator tags and their dimensions (a) 6-bits and (b) 35-bits	s 27
Figure 2.7: Basic Structure of a multiple-rectangular spiral resonator for chipless tag	28
Figure 2.8: (a) Geometry of a planar circular spiral and (b) Equivalent Circuit of the spiral	28
Figure 2.9: The hybrid coding based (a) the geometry of the tag and (b) printed tag	30
Figure 2.10: Explanation of the detection method	31
Figure 2.11: Constellation diagram for both frequency and phase deviation	31
Figure 2.12: (a) Physical structure, (b) S_{21} and (c) damping of the tag	33
Figure 2.13: (a) Setup for the multi-patch tag detection and (b) DFT of the spectrum	35
Figure 2.14: Type of chipless RFID tag exploits the properties of high-impedance surfaces	39
Figure 2.15: (a) CNR type tag and (b) resonator used in CNR tag	41
Figure 2.16: The tag detection methodology and the tag geometry (exploded view) of a CNR based RFID system	42
Figure 2.17: The communication ways between reader-tag and the data flow	44
Figure 2.18: A typical block diagram of RFID reader	45
Figure: 2.19: Analogue section (front-end) block diagram of RFID reader	45

Figure 2.20: Classification of RFID reader	46
Figure 2.21: RFID reader of (a) Bi-Static and (b) Mono-Static configuration	48
Figure 2.22: A range master multi-frequency RFID reader	50
Figure 2.23: RFID reader (a) at 2.4 GHz and (b) in UWB	53
Figure 2.24: Complete block diagram of a chipless RFID reader	55
Figure 2.25: Different existing RFID reader modules, (a) Amplitude detecting reader (2-2.5GHz), (b) Amplitude and phase detecting reader (2-2.5GHz), (c) UWB RFID reader (with 2 oscillators) working in 2-6GHz and (d) 4-8GHz reader with only one oscillator	g 56
Figure 3.1: Flow diagram of the design and development	59
Figure 3.2: The block diagram of the UWB RFID reader front-end	60
Figure 3.3: Separation of the frequencies from the mixer output	61
Figure 3.4: Detailed block diagram of the SGS	62
Figure 3.5: Circuit diagram of a Gunn diode oscillator	T
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Figure 3.6: A double balanced passive (diode) RF mixer	63
Figure 3.7: Flow diagram of the passive mixer simulation in ADS	64
Figure 3.8: Schematic design of balanced passive mixer in ADS	65
Figure 3.9: Flow diagram of a passive device simulation in ADS	66
Figure 3.10: A microstrip coupled line BPF layout for centre frequency 5.5GHz. The dimensions of 4th and 5th stage are same as 2nd and 3rd stage respectively	67
Figure 3.11: Equivalent circuit of a single couple line of the BPF	67
Figure 3.12: Basic RF switch (diode switch)	69
Figure 3.13: Schematic of a switching section that consists two switches	70
Figure 3.14: Design flow of a PA	71
Figure 3.15: S-Parameters data for PA	74
Figure 3.16: Maximum gain calculation of the PA	75
Figure 3.17: Input and output matching networks for MGA	75

Figure 3.18: Calculation of matching network	76
Figure 3.19: Matching coefficients from smith chart by ADS	76
Figure 3.20: (a) Schematic in of the PA in ADS and (b)Layout of the PA and the position of different components in ADS	77
Figure 3.21: Noise parameters for LNA	78
Figure 3.22: Matching network for LNA	79
Figure 3.23: The input/output matching parameters.	79
Figure 3.24: Smith chart solution for the LNA matching network	80
Figure 3.25: (a) Basic circuit diagram of a diode detector circuit, (b) Schematic in ADS and (c) Layout in ADS	82
Figure 3.26: (a) The single bit structure of the proposed resonator, (b) the current distribution and (c) the equivalent circuit model of the resonator	83
Figure 3.27: The design of the tag	84
Figure 4.1: (a) Master oscillator, (b) LO and (c) Mixer output in frequency domain	89
Figure 4.2: BPF S-parameter response in different frequency	90
Figure 4.3: Frequency domain response of one mixer-filter combination	91
Figure 4.4: Different biasing sequences	93
Figure 4.5: Time (a) and Frequency (b) Domain response of the switching section	94
Figure 4.6: (a) S11 and S21 response of the PA, (b) the output voltage of the PA and (c) the harmonic balance	96
Figure 4.7: Single bit resonator (a) Dimensions of a single bit resonator, (b) S_{11} Magnitude and (c) Phase	97
Figure 4.8: (a) Single resonator with transmission line, (b) S_{21} Magnitude and (c) S_{21} Phase	99
Figure 4.9: Simulated and measured results for designed resonator for the tag	100
Figure 4.10: Tag-Reader System Schematic view	102

LIST OF ABBREVIATIONS

2D	2 Dimensional
ADC	Analogue-To-Digital
ADS	Advanced Design System
AIDC	Automatic Identification and Data Capture
ASIC	Application-Specific Integrated Circuits
ASK	Amplitude Shift Keying
Auto-ID	Automatic Identification
BAP	Battery Assisted Passive
BPF	Band Pass Filter
BPSK	Binary Phase Shift Keying
BW	Band Width
CNR	Complex-Natural-Resonance
CPW	Co-planar Waveguide
CST MWS	Computer Simulation Technology Microwave Studio
CW	Continuous Wave
DFT	Discrete Furiour Transform
DPS	Density of Coding Per Surface
EAS	Electronic Article Surveillance
EIRP	Effective Isotropic Radiated Power
EM	Electromagnetic
EU	European Union
FCC	Federal Communications Commission
FD	Frequency Division
FMCW	Frequency Modulated Continuous Wave
FSS	Frequency-Selective Surface
GHz	Giga Hertz
HF	High Frequency
HIS	High-Impedance Surface
IC	Integrated-Circuit
ID	Identification
IEEE	Institute of Electrical and Electronics Engineers
IR-UWB	Impulse Radio- ultra wide band
ISM	Industrial, Scientific and Medical Radio
ISO	International standards Organizations
ITU	International Telecommunication Union

KHz	Kilo Hertz
LF	Low Frequency
LH	Left-Handed
LNA	Low Noise Amplifier
LOS	Line-of-Sight
MHz	Mega Hertz
PA	Power Amplifier
RF	Radio Frequency
RFID	Radio Frequency Identification
R _X	Receiver
SAW	Surface Acoustic Wave
SEM	Singularity Expansion Method
SSI	Selective Spectral Interrogation
STMPM	Short-Time Matrix Pencil Method
TD	Time Division
TDR	Time-Domain Reflectometry
T _X	Transmitter
UHF	Ultra High Frequency
UWB	Ultra Wide Band
VCO	Voltage Controlled Oscillator
VNA	Vector Network Analyser
WPAN	Wireless Personal Area Networks

LIST OF SYMBOLS

+	Plus
-	Minus
>	Greater Than
<	Smaller Than
\leq	Smaller Than or Equal
S	Spectral Density
Pt	Transmitted Power (Reader Antenna)
Gr	Antenna Gain (Reader)
π	Pi
r	Radius
Pa	Power At Tag Antenna
λ	Lamda (Wavelength)
A _e	Effective Aperture
Gt	Antenna Gain (Tag)
P _{rx}	Backscattering Received Signal
L _T	Total Distributed Inductance
CT	Total Capacitance of Spiral
R	Resistance
f _{res}	Resonant Frequency
C_0	Total Distributed Capacitance of One Turn Spiral
r _m	The Mean Radius
R _{in}	Inner Radius
R _{out}	Outer Radius
Ls	Self-Inductance of The Spiral
Μ	The Mutual Inductance Between Turns of The Spiral
$y(\theta, \varphi, t)$	Response in Complex Frequency Plane
$R_i(\theta, \varphi)$	Radius of Complex Amplitude
j	Complex Parameter
ω	Angular Frequency
α_i	Damping Factor
Si	The Poles
$\sum R_i$	Summation
i=1	Phase Angle
U	r llase Allge

 φ Amplitude

CHAPTER ONE INTRODUCTION

1.1 BACKGROUND

RFID (Radio frequency identification) is mainly a technique of transmitting and receiving necessary data between an RFID tag and reader which uses radio frequency for detection. It comes from one of the branches of the term called 'Automatic Identification (Auto-ID)' named by the organization called Automatic Identification and Data Capture (Su, Chu, Prabhu, & Gadh, 2007). Auto-ID system has several branches such as optical barcode, biometric, smart card, RFID and other systems. Commonly, the barcode and RFID system are involved in tagging of the object or product. The optical barcode is comparatively cheaper and can be labelled on any of the products to be identified. Though the optical bar code system is cheaper, it has several disadvantages. It needs a line of sight in communication. It malfunctions due to dirt, damp, wear and tear. It has always risk of tempering. Moreover, it cannot be fully automated and needs manual operation for its function. On the other hand, the RFID depends on data transmission by means of radio frequency signals, and subsequently, the whole operation is line-of-sight (LOS) and weather independent. A full automatic system can be possible to be developed by using RFID. As a result, RFID is a potential and better candidate compared to the barcode for its object tagging and tracking abilities (McFarlane & Sheffi, 2003).

Since silicon chips are fabricated on a wafer-by-wafer basis, there is a fixed cost per wafer that is independent of the integrated-circuit (IC) design thus the cost of the RFID chip can be estimated based on the required silicon area for the RFID chip. Hence, with highly optimized low transistor count application-specific integrated

circuits (ASICs), implemented assembly processes and extremely large quantities (over 1 billion) of RFID chips sold per annum, a minimum cost of 5 cents is a reasonable estimate for chipped RFID tags (Ahsan, Shah, & Kingston, 2010).

The major application of RFID technology including manufacturing processes, supply chain management, inventory control in warehouse, counterfeit prevention in bank notes and secured documentation are established. Scientists and researchers throughout the world have been trying to resolve some current issues involved in the RFID system such as costing, reliability, security, and standardizing before being implemented in the relevant sectors. Therefore, there is a lot of scope to do research in this field of RFID.

Item tracking and identification is one of the major challenges of RFID because this market represents 10 trillions of units sold each year. Competing with the optical barcode technology is a hard task for classical RFID tag having an antenna and a chip, whereas chipless RFID tag has a key role to reach this goal. Indeed, this emerging technology is under growing interest and many research projects are under development.

RFID systems can be classified in many ways. On the basis of its frequency of operation it is low frequency (125 KHz), high frequency (13.56MHz), ultra high frequency (860 to 930 MHz), microwave (2.45 GHz) and ultra wide band (UWB) that working between 3.1 to 10.7 GHz. In each working band of frequencies different detection techniques and topologies are adopted while designing the system. Moreover, the dimensions of the tag are also a function of the operating frequency (Butters & Consultant, 2006).

Similarly, on the basis of on board tag power supply it is classified as: active type tag realises of its transmission, reception and other functions on the battery on-board,

semi-active type tag that also has battery on board only to keep the digital circuitry alive but needs the assistance of reader interrogation signal power to send and receive the information and passive type tag which comprises no battery on board and totally dependent on the interrogation signal power to perform all necessary operations. The chipless tags don't carry any silicon chip on-board so, it is inherently passive in nature. Since there is no inclusion of chip on board the chipless is the main competitor to replace the conventional barcode system.

Nowadays, a number of active and passive RFID systems in different frequencies are available in the market. Hence, a wide range of RFID tag and their corresponding reader are available in the commercial market. At present, chipless RFID technology opens a new research area in RFID due to its simplicity and low cost since a chipless tag doesn't hold any silicon chip and no classical RF modulation schemes are required for communication (Garfinkel, Juels, & Pappu, 2005). The UWB enables a wide frequency range for the researchers to design different chipless RFID systems and UWB RFID systems are now at the forefront of the research. However, the UWB RFID technology is not omnipresent and yet to be fully deployed commercially.

There are mainly two significant parts of the RFID system, tag and reader. Basic mechanism of the RFID, the reader or interrogator sends the RF interrogation signal and in response, the tag transmits the data stored in it. The architecture of an RFID reader absolutely depends on the type of tag used. However, the main components of RFID reader are antenna(s), analogue section that consist of transmitter and a receiver, and the digital section. Usually the analogue section is called the front-end of the reader. Figure 1.1 shows the basic block diagram of an RFID system.



Figure 1.1: The basic block diagram of an RFID system

The front-end is connected with the transmitting and receiving antenna(s) as the gateway of the reader. Mainly, the construction of the reader front-end is based on the type of the tag and also the frequency of the operation. For the chipless type resonator tag, the reader transmitter consists of VCO (voltage controlled oscillator), band pass filter (BPF) and a power amplifier (PA). The VCO generates a linear chirp (swept) frequency interrogation signal. The reader, which is a coherent frequency-modulated continuous-wave device, interrogates a tag and decodes the tag's identification data from its both amplitude and phase information (Preradovic, Karmakar, & Zenere, 2010). The tag and reader are integrated part in an RFID research and there is no scope to consider them separately.

Chipless tag is inherently passive and attractive to the end users since they do not have an on-board power supply and therefore rely only on the power emitted from the reader. Passive tags may or may not contain an integrated circuit, memory block, or application specific integrated circuit.

Chipless tags are usually in the form of electronic article surveillance (EAS) or surface acoustic wave (SAW). It can be used in retail shops for auto inventory management and security purposes. Basically the chipless RFID tags are exploiting the physical effects of the tag's design. All chipless RFID tags differ fundamentally from tags which contain an IC, in terms of operational principles and power consumption. The tag itself generates data due to its physical architecture and design, and therefore, it represents a unique ID device.

Nowadays, the chipless RFID reader approaches are complex, bulky, expensive and commercially inadequate in the market. Their existences are still in the incubation stages inside the research lab. Though the chipless tag price is going lower, however the price of the reader is still very high and the dimension of reader is also bulky. As a result, these are the big issues at present to the RFID end users. An UWB RFID reader is an RF transceiver. Refers to Figure 1.1, the circuitry of the RFID reader is complex in design. This actually makes the system bulky, costly and complex. One of the solutions is to use the RF switch but, in this case implementation of the switch cannot be feasible because the reader keeps sending the CW signal while receiving the backscatter signal from the tag. Also because of the large bandwidth of the UWB system the generation of the constant analogue chip signal is very difficult and difficult to find in the existing market. This makes a conclusion that one reader can be made in such a way that there is no constant chirp but only the corresponding signals related to the tag.

1.2 PROBLEM STATEMENT

The authors in Jalaly et al., (2005) presented a tag that encodes 5 bits with 5 microstrip dipole resonators. The data was encoded by controlling the presence/absence of resonance (based on the resonator) at a known frequency. Using the same coding technique, Preradovic et al, (2009), has proposed a tag based on multiple spiral resonators and was able to encode 35 bits. This design is efficient in

terms of coding capacity but needs a large size (7 cm 15 cm) since one bit is equal to one resonator. The phase can also be used to encode data and several techniques have been experimented. Mukherjee et al., (2011) uses a wideband antenna as a reflector connected to a complex load that can produce different phase profiles.

For each phase profile an identifier is associated. The authors in Balbin & Karmakar, (2009), used multiple patch antennas connected to a stub of variable length and encodes data by varying the phase of each antenna independently. However, at the present time, coding capacity is not significant (few bits) and it is still a big challenge to embed a large number of data into a tag of size similar to that of a credit card (5 cm 8 cm). Despite many proposals are made, due to the space dependency, the UWB passive RFID tag dimension is still relatively large to fully replace the conventional barcode.

In classical RFID, anti-collision management is a part of the protocol but in case of the chipless tag that cannot be implemented. However some techniques based on time or spatial separation of tags can be used. An antenna with narrow beam width can be designed for this purpose, but the main weaknesses of chipless technology concerns their non-rewritable capability and their data capacity limited to a few tens of bits. This last characteristic could be a limiting factor for many applications, and this is why work has to be done to increase the data capacity of chipless tags.

In the reader front-end topologies, there are two configurations omnipresent i.e. two antenna (bi-static) and single antenna (mono-static) configuration. In most of the lower frequencies (until UHF band) the mono-static antenna is established. In UWB still the bi-static configuration is not replaced by the mono-static only because of the separation of the transmitted and received signal. However, bi-static antenna based front-end configurations for the RFID reader, imposes few problems such as

Bi-static antenna system makes the system bulky. Use of two separate antennas for transmission and reception makes the system almost double costly. Implementation of the bi-static antenna system is sophisticated, so, the design becomes complex. Two antennas always make crosstalk/isolation problem.

There are a few drawbacks, which are significant, associated with chipless RFIDs. No protocol is followed with this type of RFID; therefore, collision is a major challenge. The required reader would need more sophisticated architecture and will be more expensive than existing commercial readers. The reader's transmitted power is limited by the Federal Communications Commission, FCC, Section 15.247 to an effective isotropic radiated power (EIRP) of 1 Watt. At practical operating distances, the chipless RFID has a clear advantage, requiring less radiated power.

Often, it has been seen from literature that in the RFID system, the main focus is on the tag and how it can be read. Many researchers tried to read the backscattered data from the passive UWB tag with highly sophisticated test instrumentations such as, VNA (vector network analysers), spectrum analysers etc. In different literatures, the authors tried to propose the UWB RFID readers that can successfully read the microstrip resonator type tag. Where, the generation of the chirp signal (and also in the tag space) limits the inclusion of more bits to the system. Inclusion of more bits needs more bandwidth which leads to the difficulties to design large bandwidth chirp signal. In our knowledge there are very little emphasis given on the development on the reader while developing a RFID system. Moreover, due to the large bandwidth of the system, to generate the constant amplitude chirp signal, the design of the VCO is very difficult to implement for the reader front end of UWB passive RFID tag.